

MOTOROLA

*Communications and Advanced
Consumer Technologies Group*

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MCF5206UMAD/AD

MCF5206

Errata to
MCF5206
User Manual

March 20, 1997

This errata to the initial release of the **MCF5206 UM/AD** User's Manual provides corrections to the original printed text. This document and other information on this product is maintained on the World Wide Web at <http://pirs.aus.sps.mot.com>

Instruction Processing

I2 - The MOVEC opcode to load the Vector Base Register (VBR) does not function correctly.


Workaround:

Use the following code to load the VBR:

```
MOVE.W#$2700, SR# disable interrupts
MOVE.La7,<mem># save current stack pointer
MOVE.LVBR, a7# load desired VBR into A7
MOVE.La7, vbr# load the Vector Base Register
MOVE.L<mem>, a7# restore the stack pointer
MOVE.W#$2000, SR# enable interrupts
```

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